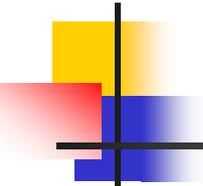


# MEMS: Fabrication

## Lecture 5: Micromachining



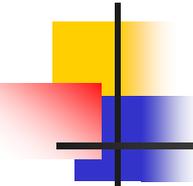
Prasanna S. Gandhi  
Assistant Professor,  
Department of Mechanical Engineering,  
Indian Institute of Technology, Bombay,



# Recap: Last Class

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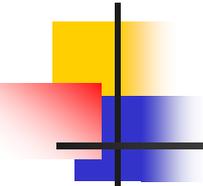
- E-beam lithography
- X-ray lithography
- Ion beam lithography
- Oxidation



# Today's Class

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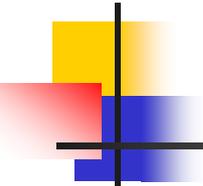
- Clean room fundamentals
- Si wafer preparation
- Chemical etching process
- Anisotropic Etching
- Silicon micromachining
  - Surface micromachining
  - Bulk micromachining
  - How to produce devices



# Clean Room Fundamentals

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- Need
- Class of a clean environment
- Class X clean room → not more than X particles (of size  $0.5\mu\text{m}$  or larger) per cubic foot of air
- How this cleanliness is produced and maintained??



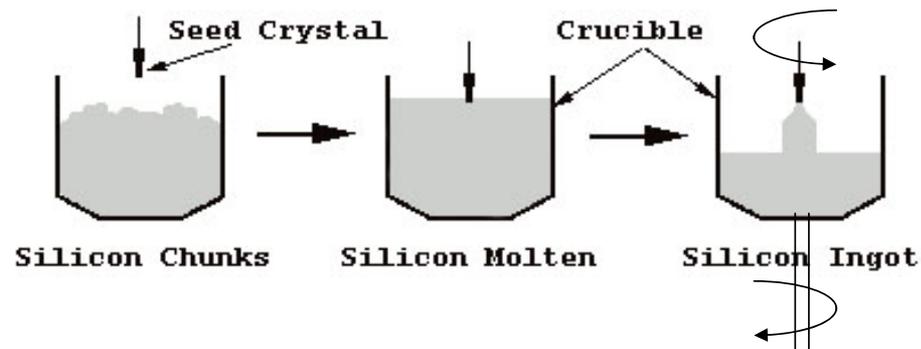
# Clean Room Fundamentals

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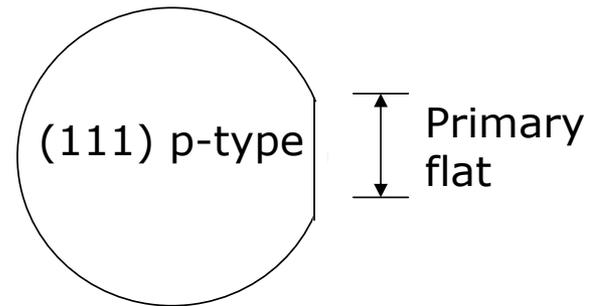
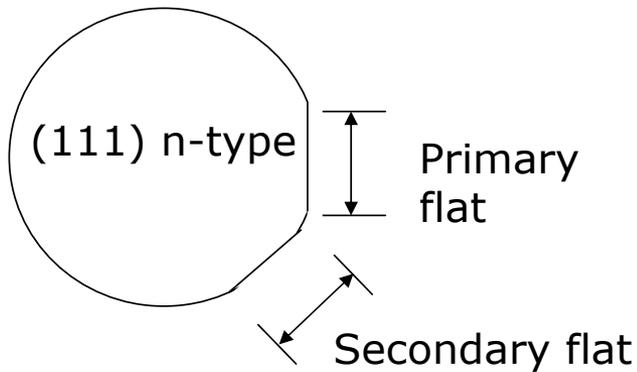
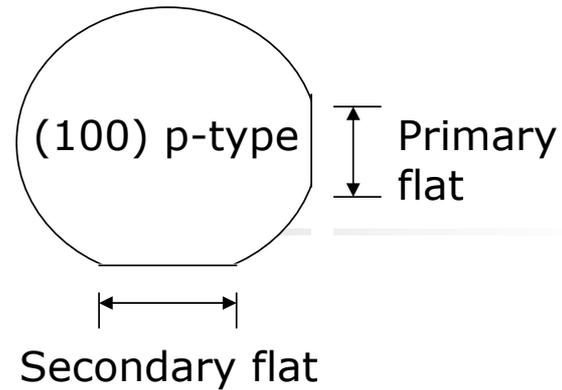
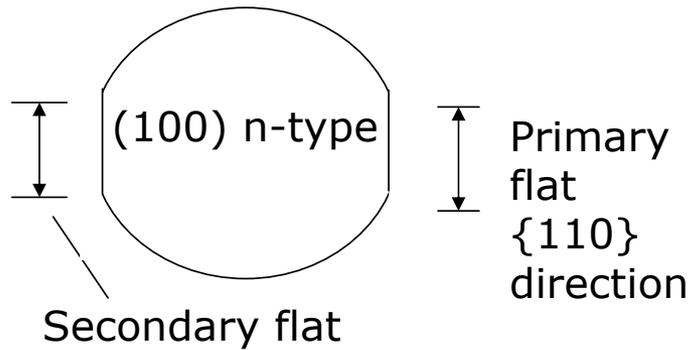
- Air conditioning plant
- HEPA filters air recirculation through these filters

# Si-wafer preparation

- Czochralski process



- Cutting, CMP, cleaning



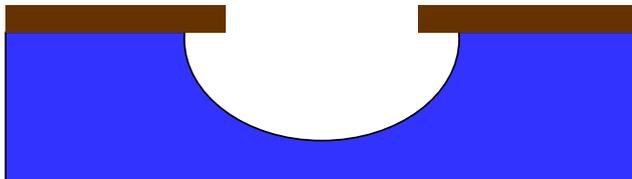
Primary and Secondary wafer flats are used to identify orientation and type.

# Chemical Etching

Without agitation (5)



With agitation (20)

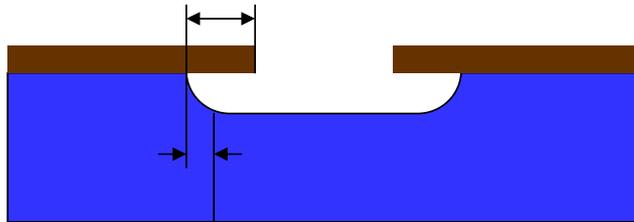


## ■ Isotropic etching

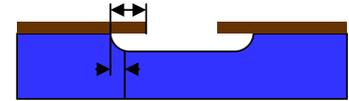
- Etchant: HNA mixture.
- HNA can dissolve 550 $\mu\text{m}$  thick silicon wafer in about 20 min.
- HNA mixture removes silicon equally in all directions.
- SiO<sub>2</sub> etch: 10-30nm/min

# Chemical Etching

Without agitation (5)

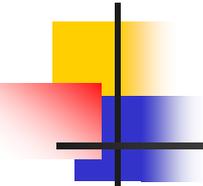


- Isotropic etching
  - Undercut
  - Etch bias
- Materials & etchants\*



# Etch Stop Mechanisms

- Time etch stop
- Dopant B+ (heavy dope) as etch stop
  - Pg 45 Spoek
- Thin films
- Electrochemical etch stop
  
- Anisotropic Etching planes



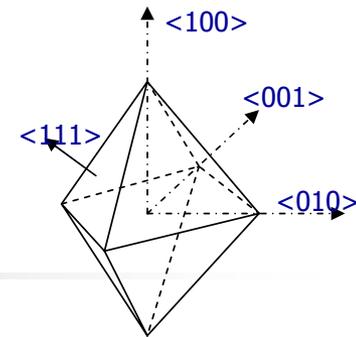
# Chemical Etching

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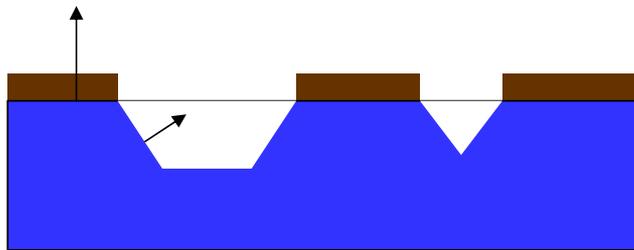
Choice of etchant:

- Etch rate
- Topology of the surface to be etched
- Etch selectivity of mask material and other materials
- Toxicity
- Ease of handling

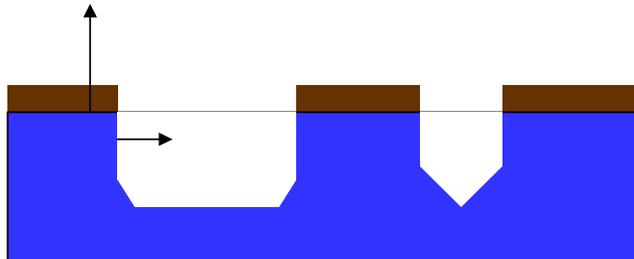
# Chemical Etching



$\langle 100 \rangle$  surface wafer



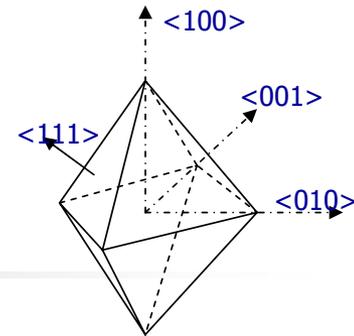
$\langle 110 \rangle$  surface wafer



- Anisotropic bulk etching

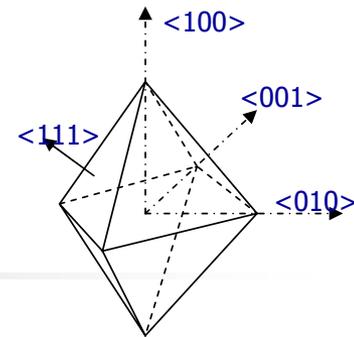
- Etchant: KOH, EDP (ethylen diamine pyrocatechol), TMAH (Tetra methyl ammonium hydroxide)
- $\langle 111 \rangle$  direction has lower etching rates than  $\langle 100 \rangle$
- Can produce grooves, slanted/vertical walls

# Chemical Etching



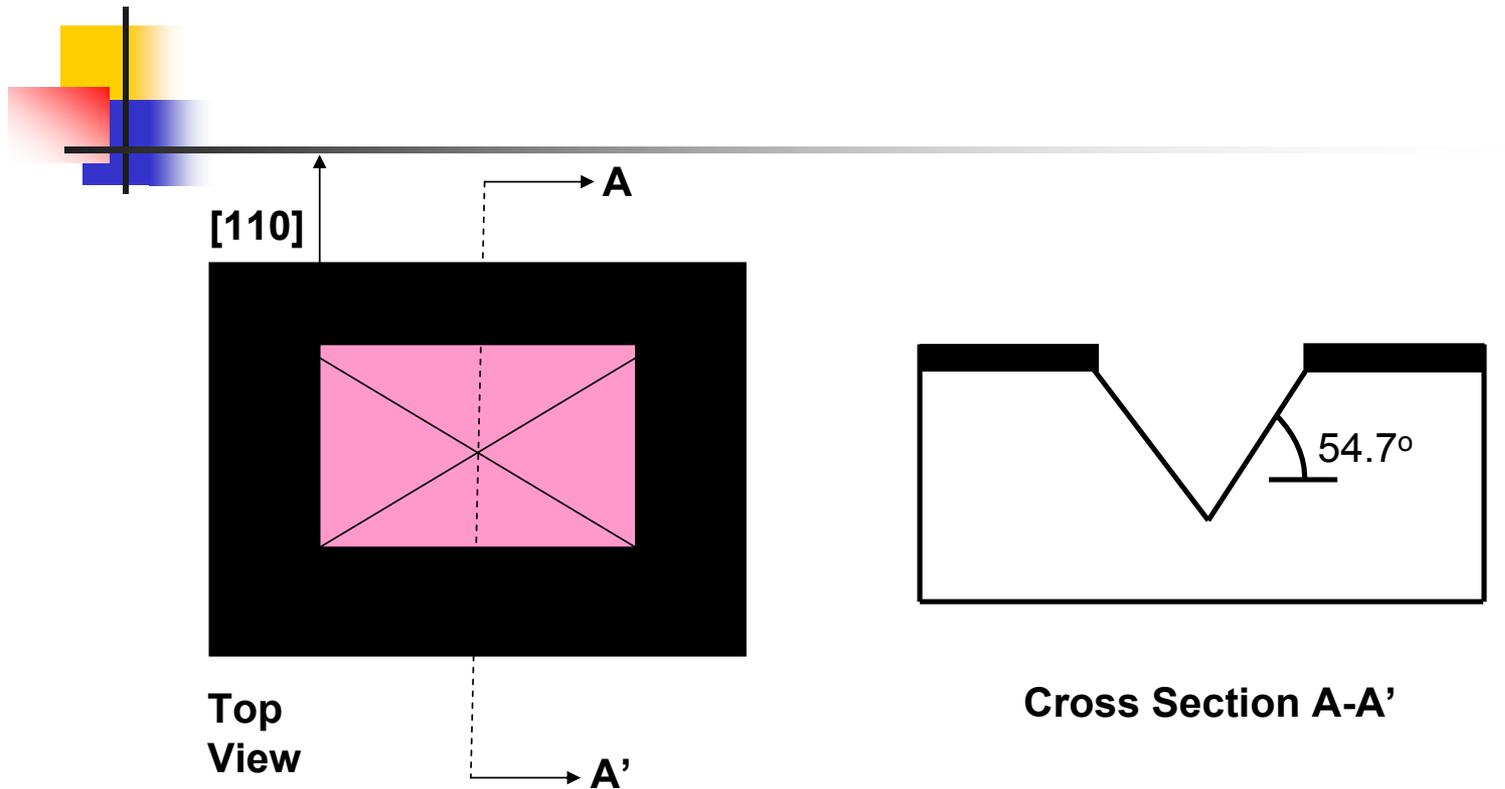
- Silicon crystal geometry\*
- Examples of use of the crystal geometry in etching
- Fundes regarding etch shapes under different conditions\*

# Anisotropic Etching



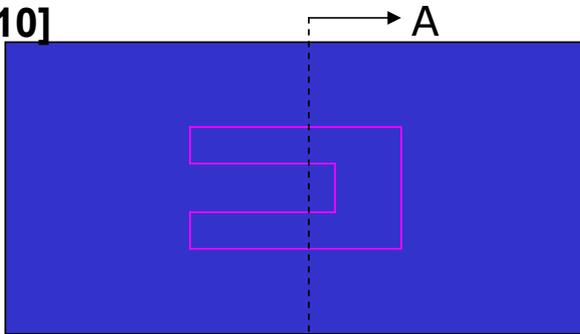
KOH, EDP and TMAH

- EDP etches oxide 100 times slower than KOH,
- KOH, TMAH dangerous to eye
- KOH less dangerous than EDP & TMAH
- Etch curves\*: 5hrs to etch 300 $\mu$ m thick wafer
- H<sub>2</sub> bubbles during KOH etching of Si
- EDP ages quickly in contact with oxygen producing red brown color, vapor is harmful
- HF dip is necessary for EDP: native oxide problem

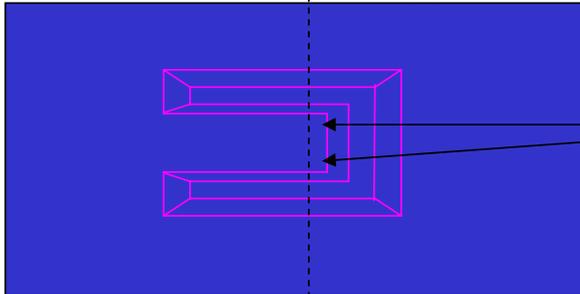


A square  $\langle 110 \rangle$ -oriented mask feature results in a pyramidal pit.

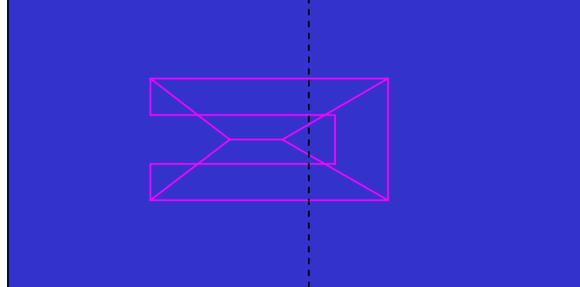
[110]



A'  
A

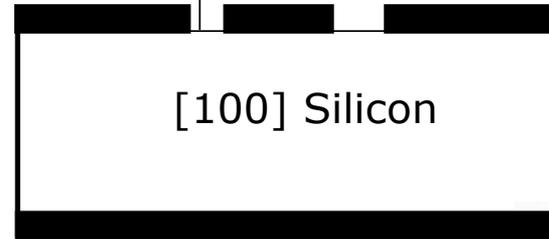


A'  
A



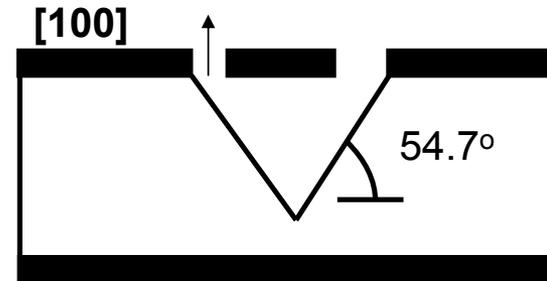
A'

[100] Masking Layer



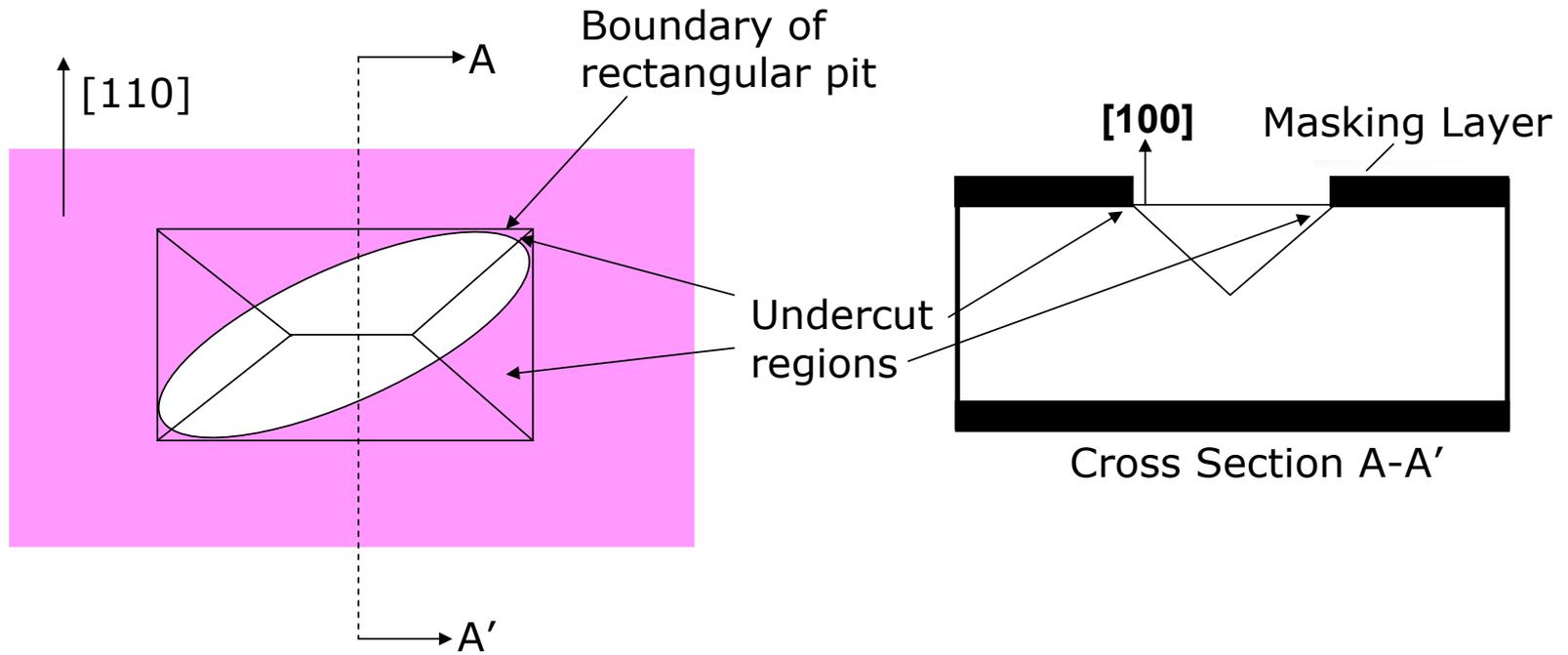
Cross Section A-A'

Convex corners are rapidly undercut

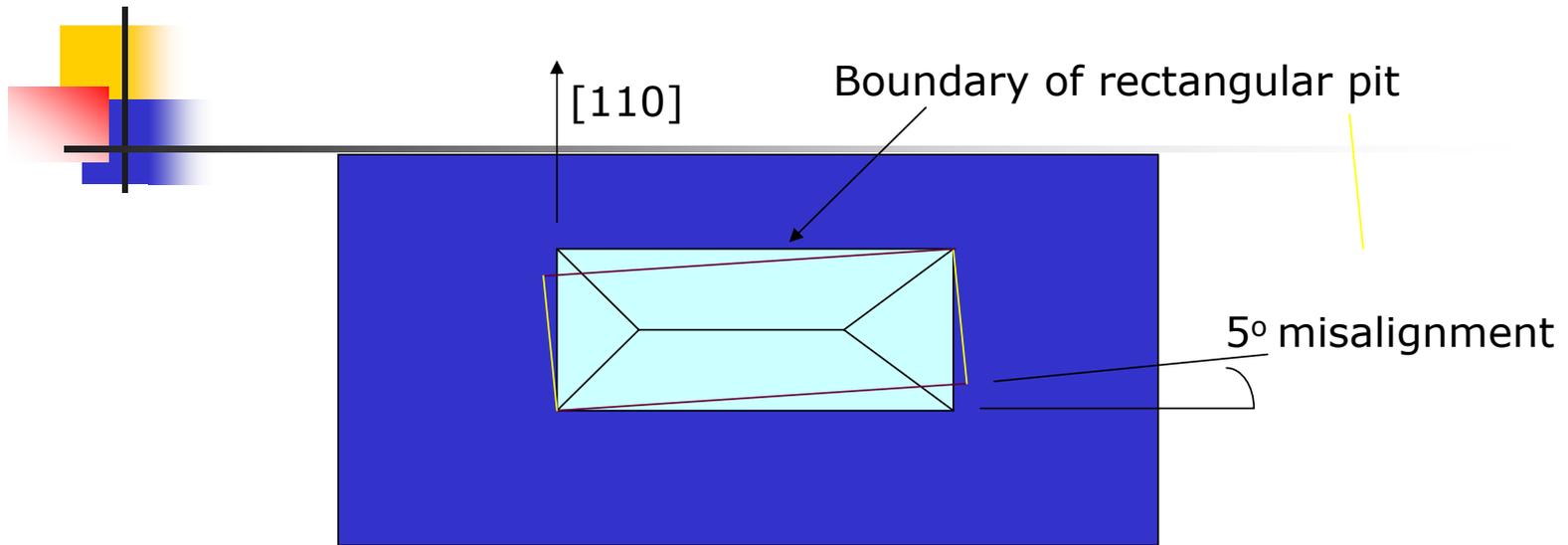


Cross Section A-A'

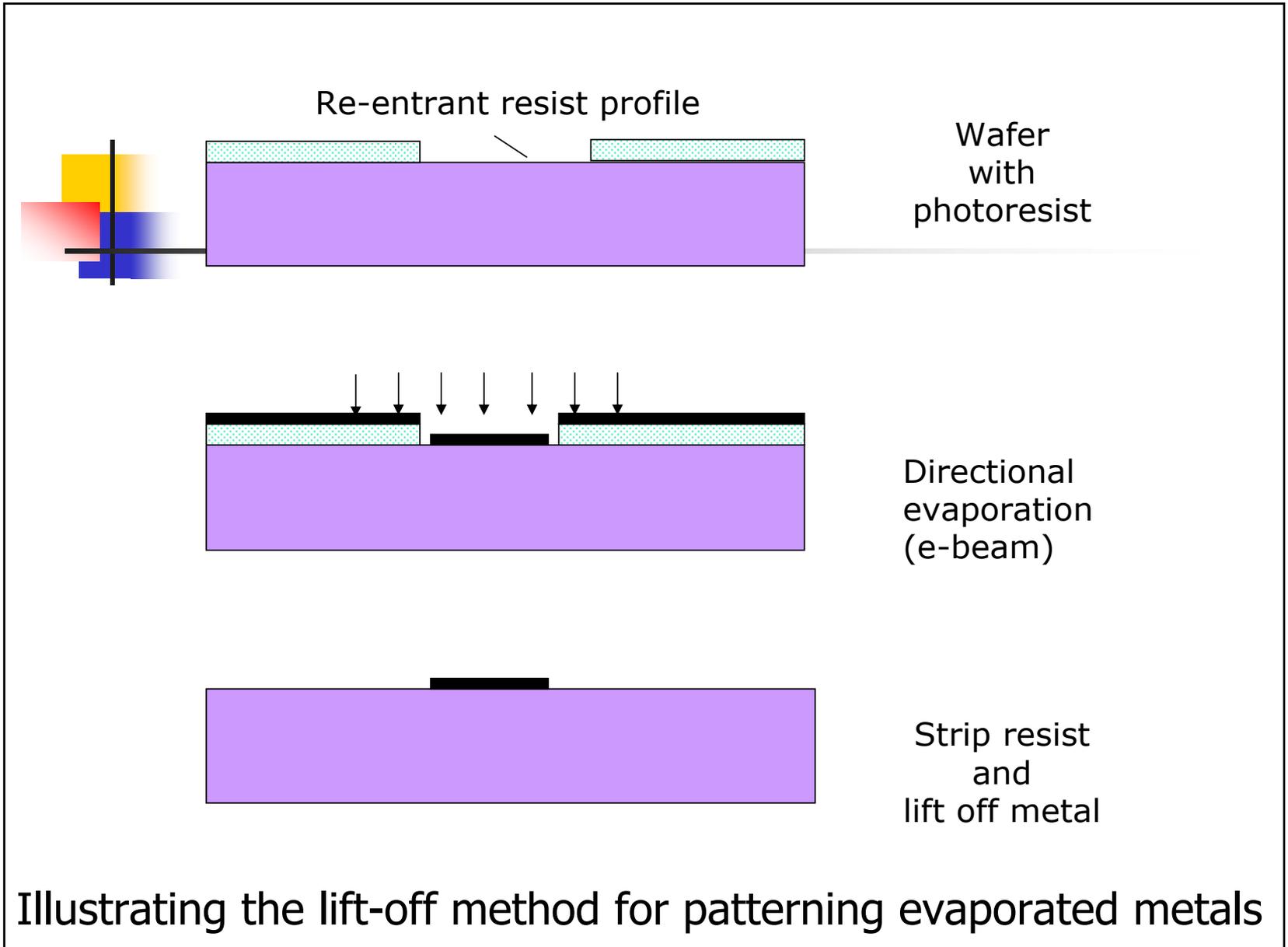
Convex corners where  $\{111\}$  planes meet are not stable. They are rapidly undercut. This permits creation of suspended structures.



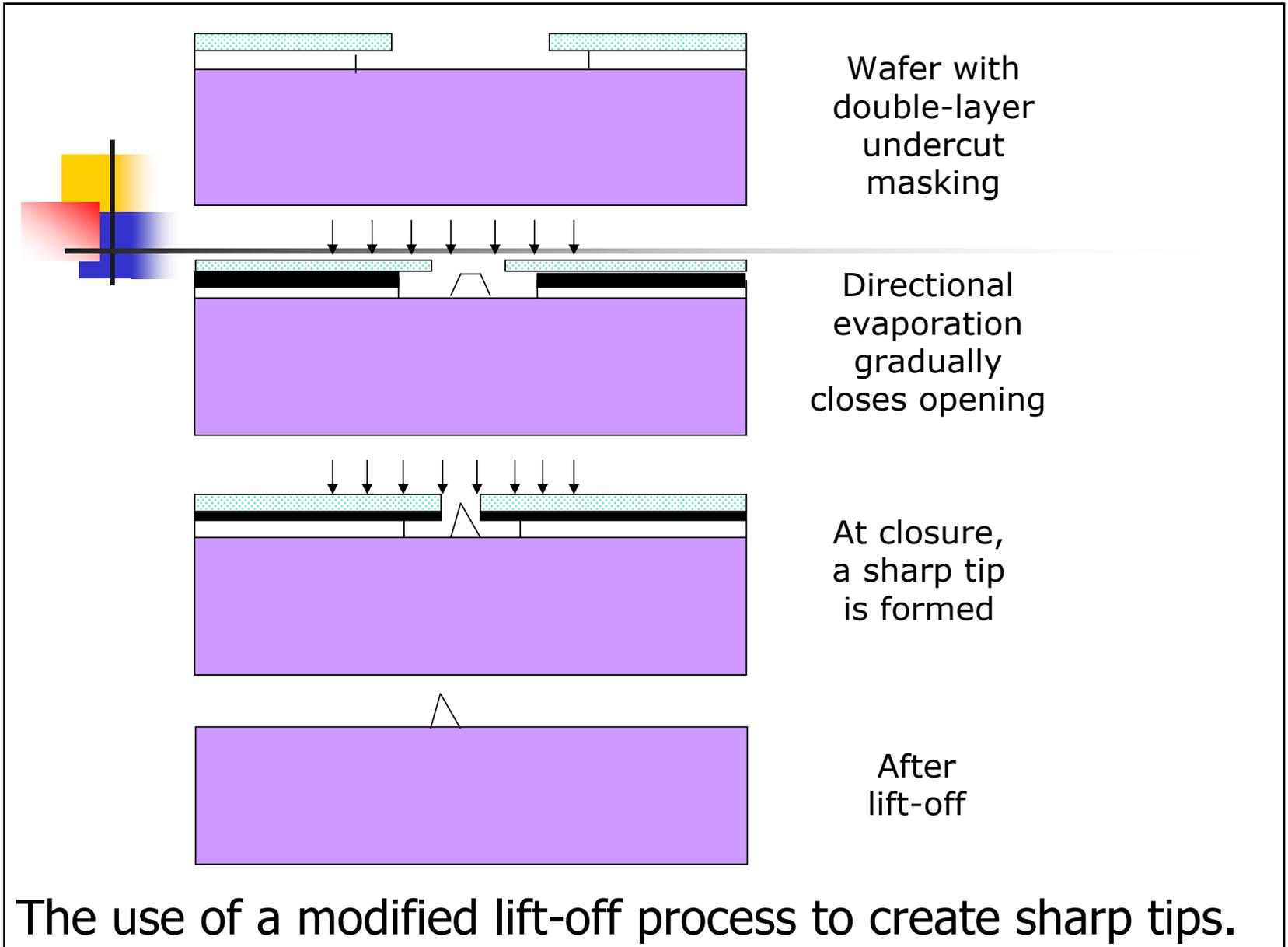
Any mask-layer feature, if etched long enough, will result in a rectangular V-groove pit beneath a rectangular that is tangent to the mask features, with edges oriented along  $\langle 110 \rangle$  directions.

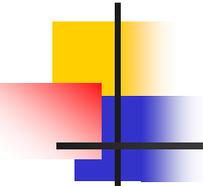


The effect of misalignment is to enlarge the etched region. This figure shows the effect of a 5° misalignment for a rectangular feature.



Illustrating the lift-off method for patterning evaporated metals

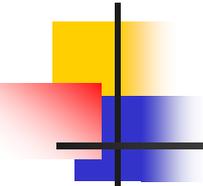




# Conclusions

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- Chemical etching
  - Isotropic
  - Anisotropic
- Bulk and Surface micromachining
- Etch stop mechanisms
- Liftoff process

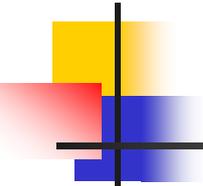


# Next class

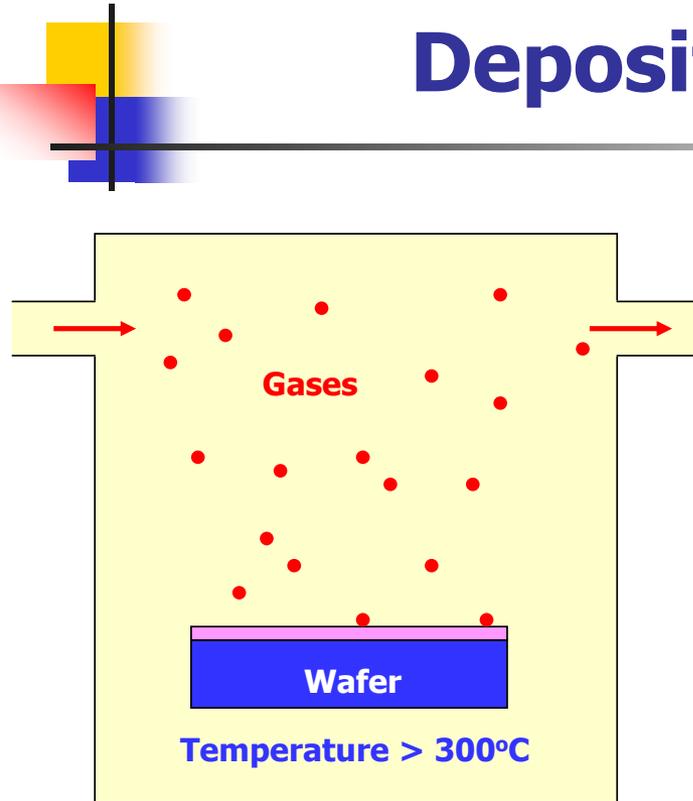
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- Plasma based processes
  - Plasma etching
  - RIE
  
- Sputtering
- PE CVD

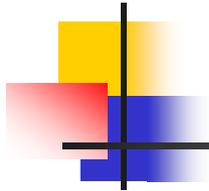
# Wafer Cleaning Process

- 
- Need
  - CMP- Chemical mechanical polishing
  - RCA cleaning

# Chemical Vapor Deposition (CVD)



- Chemical reaction in vacuum chamber
- High temperatures ( $>300^{\circ}\text{C}$ )
- Polysilicon,  $\text{SiO}_2$ ,  $\text{Si}_3\text{N}_4$ , tungston, titanium, copper etc. can be deposited
- Low pressure CVD (LPCVD)
- Plasma Enhanced CVD: low temperatures
- Pressure, temp, gas flow



$$x = \frac{B}{A}(t + \tau)$$

For small time  $t$ , and

$$x = \sqrt{B(t + \tau)}$$

for large time  $t$ , where  $\chi$  is the thickness of the oxide layer in the silicon substrate in micrometers at time  $t$ , in hours.  $A$  and  $B$  are constants, and the parameter  $\tau$  can be obtained by:

$$\tau = \frac{\left( \frac{d_0^2 + 2Dd_0}{k_s} \right)}{2DN_0}$$

where

- $D$ =diffusivity of oxide in silicon, e.g.,
- $D=4.4 \times 10^{-16} \text{cm}^2/\text{s}$  at  $900^\circ\text{C}$
- $d_0$ =initial oxide layer ( $\sim 200$  in dry oxidation,  $=0$  for wet oxidation)
- $k_s$ =surface reaction rate constant
- $N_0$ =concentration of oxygen molecules in the carrier gas
- $=5.2 \times 10^{16} \text{molecules}/\text{cm}^3$  in dry  $\text{O}_2$  at  $1000^\circ\text{C}$  and  $1 \text{atm}$
- $=3000 \times 10^{16} \text{molecules}/\text{cm}^3$  in water vapor at the same temperature and pressure
- $N_1$ =number of oxidizing species in the oxide
- $=2.2 \times 10^{22} \text{SiO}_2 \text{ molecules}/\text{cm}^3$  in dry  $\text{O}_2$
- $=4.4 \times 10^{22} \text{SiO}_2 \text{ molecules}/\text{cm}^3$  in water vapor